

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 4

CHART **DATA** **REPORT** **SEARCH** **HELP**

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| Application Number | 09/766,162 |
| Filing Date | 01-19-2001 |
| First Named Inventor | Donald S. Gardner |
| Art Unit | 2832 |
| Examiner Name | Not yet assigned |
| Attorney Docket Number | 42390P10775 |

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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| Examiner Signature | Troyer T. Nguyen | Date Considered | 10/31/02 |
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| Examiner Initials | Cite No. 1 | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published |
| TIN | | K. SHIRAKAWA, ET AL., "Thin Film Cloth-Structured Inductor For Magnetic Integrated Circuit," IEEE Transactions on Magnetics, September 1990, pp. 2262-2264, Vol. 26, No. 5. |
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| | | ERIK J. BRANDON, "Passive Components For Systems-On-A-Chip Applications," Center for Integrated Space Microsystems, Jet Propulsion Laboratory. |
| TIN | | S.S. MOHAN, ET AL., "Simple Accurate Expressions For Planar Spiral Inductances," IEEE Journal of Solid-State Circuits, October 1999, pp. 1419-1424, Vol. 34, No. 10. |
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| TIN | | M. YAMAGUCHI, ET AL., "MGHz-Drive Magnetic Thin-Film Inductors For RF Integrated Circuits Using Micro-Patterned Granular Film" IEEE, 1990. |
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| TIN | | DONALD S. GARDNER and PAUL A. FLINN, "Mechanical Stress As A Function Of Temperature For Aluminum Alloy Films," Journal of Applied Physics, February 15, 1990, pp. 1831-1845, Vol. 67. |
| TIN | | M. BABA, ET AL., "GHz-Drive Magnetic Thin-Film Inductor Using CoNbZr Film," Journal of the Magnetics Society of Japan, 2000. |
| | | No MONTH |

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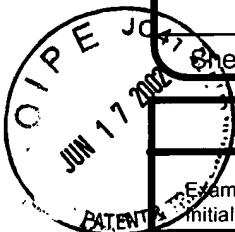
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| TIN | | Y. KOBAYASHI, ET AL., "New Type Micro Cloth-Inductor And Transformer With Thin Amorphous Wires And Multi-Thin Coils," IEEE Transactions on Magnetics, September 1992, pp. 3012-3014, Vol. 28, No. 5. | |
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| TIN | | V. KORENIVSKI and R.B. VAN DOVER, "Magnetic Film Inductors For Radio Frequency Applications," Journal of Applied Physics, November 15, 1997, pp. 5247-5254, Vol. 82. | |
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| | | JOACHIM N. BURGHARTZ, <u>"Progress In RF Inductors On Silicon - Understanding Substrate Losses,"</u> IBM Research Division, Yorktown Heights, NY. | |
| TIN | | S. YABUKAMI, ET AL., "Noise Analysis Of A MHz-3 GHz Magnetic Thin Film Permeance Meter," Journal of Applied Physics, April 15, 1999, pp. 5148-5150, Vol. 85. No. 8. | |
| | | JAE PARK and MARK G. ALLEN, <u>"Bar-Type Microinductors and Microtransformers With Electroplated Alloy Cores,"</u> Magnetic Devices Research, sponsored by Packaging Research Center. | |

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| Examiner Initials* | Cite No. 1 | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|--------------------|------------|---|----------------|
| | | <u>ERIK BRANDON, "System On A Chip Integrated Passive Components (μIRS)"</u> | DOM |
| <i>TIN</i> | | MASAHIRO YAMAGUCHI, "Magnetic Films For Planar Inductive Components And Devices," Handbook of Thin Film Devices, edited by M.H. Francombe, 2000, pp. 185-186, Vol. 4: Magnetic Thin Film Devices. | |
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| | | <u>S.S. MOHAN, ET AL., "Modeling And Characterization Of On-Chip Transformers,"</u> Center for Integrated Systems, Stanford University, Stanford, CA 94305. | |
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| <i>TIN</i> | | TERENCE O'DONNEL, ET AL., "Microtransformers and Inductors Using Permalloy Thin Films," Preparation, Properties, and Applications of Thin Ferromagnetic Films, June 2000, pp. 45-52. | |
| <i>TIN</i> | | C. PATRICK YUE and S. SIMON WONG, "On-Chip Spiral Inductors With Patterned Ground Shields For Si-Based RF IC's," IEEE Journal of Solid-State Circuits, May 1998, pp. 743-752, Vol. 33, No. 5. | |
| | | <u>DONALD S. GARDNER, United States Patent Application for "Method and Apparatus for Providing Inductor for Integrated Circuit or Integrated Circuit Package"</u> . | |
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